

COURSE OUTLINE

(1) GENERAL

SCHOOL	SCHOOL OF SCIENCES		
ACADEMIC UNIT	PHYSICS DEPARTMENT		
LEVEL OF STUDIES	GRADUATE		
COURSE CODE	510	SEMESTER	7
COURSE TITLE	MODERN PROGRAMABLE ELECTRONICS		
INDEPENDENT TEACHING ACTIVITIES <i>if credits are awarded for separate components of the course, e.g. lectures, laboratory exercises, etc. If the credits are awarded for the whole of the course, give the weekly teaching hours and the total credits</i>	WEEKLY TEACHING HOURS	CREDITS	
	4	4	
<i>Add rows if necessary. The organisation of teaching and the teaching methods used are described in detail at (d).</i>			
COURSE TYPE <i>general background, special background, specialised general knowledge, skills development</i>	Skills development		
PREREQUISITE COURSES:			
LANGUAGE OF INSTRUCTION and EXAMINATIONS:	Greek		
IS THE COURSE OFFERED TO ERASMUS STUDENTS	Yes(Greek)		
COURSE WEBSITE (URL)			

(2) LEARNING OUTCOMES

<p>Learning outcomes</p> <p><i>The course learning outcomes, specific knowledge, skills and competences of an appropriate level, which the students will acquire with the successful completion of the course are described.</i></p> <p><i>Consult Appendix A</i></p> <ul style="list-style-type: none"> • <i>Description of the level of learning outcomes for each qualifications cycle, according to the Qualifications Framework of the European Higher Education Area</i> • <i>Descriptors for Levels 6, 7 & 8 of the European Qualifications Framework for Lifelong Learning and Appendix B</i> • <i>Guidelines for writing Learning Outcomes</i>
<p>This course aims to introduce students to modern programmable electronic devices. It includes theory and a series of laboratory exercises that constitute a complete set of experiments. The theory combined with laboratory exercises beyond the scientific knowledge will provide students with a valuable practical resource useful for their further research or professional career. There will be emphasis on the theory and application of programmable integrated circuits (FPGAs) and microcontrollers (μC). After the completion of theory and laboratory exercises the student:</p> <ul style="list-style-type: none"> • Will be able to program a modern electronic device (FPGA / μC) and control a simple system • Have acquired practical skills in programming and testing digital systems on FPGA development boards and microcontrollers. • He / she will have a very good knowledge of the processes that are

performed in the programable module

- Understand the operation of various I / O devices and how they are controlled and communicated with the programmable electronic device
- Have knowledge of an integrated development environment (IDE) suitable for simulating, debugging and implementing a physical layout for testing and construction.

General Competences

Taking into consideration the general competences that the degree-holder must acquire (as these appear in the Diploma Supplement and appear below), at which of the following does the course aim?

Search for, analysis and synthesis of data and information, with the use of the necessary technology

Adapting to new situations

Decision-making

Working independently

Team work

Working in an international environment

Working in an interdisciplinary environment

Production of new research ideas

Project planning and management

Respect for difference and multiculturalism

Respect for the natural environment

Showing social, professional and ethical responsibility and sensitivity to gender issues

Criticism and self-criticism

Production of free, creative and inductive thinking

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Others...

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Working independently

Decision-making

Team work

Search for, analysis and synthesis of data and information, with the use of the necessary technology

(3) SYLLABUS

Introduction to programmable electronics. Laboratories: Measurements using programmable electronics, photodiodes / switches interface, imaging applications, serial / parallel data transfer, encoding / decoding, multiplexing, memory circuits, registers, counters, timing, waveform generator.

(4) TEACHING and LEARNING METHODS - EVALUATION

DELIVERY <i>Face-to-face, Distance learning, etc.</i>	Face-to-face learning	
USE OF INFORMATION AND COMMUNICATIONS TECHNOLOGY <i>Use of ICT in teaching, laboratory education, communication with students</i>	<ul style="list-style-type: none"> • All information is provided to the student about the announcements, course scheduling, course regulation, etc through the e-course asynchronous tele-learning system • During the laboratory training an integrated development environment suitable for the design, simulation, debugging and implementation of the workout exercises is used. 	
TEACHING METHODS	<i>Activity</i>	<i>Semester workload</i>

<p>The manner and methods of teaching are described in detail. Lectures, seminars, laboratory practice, fieldwork, study and analysis of bibliography, tutorials, placements, clinical practice, art workshop, interactive teaching, educational visits, project, essay writing, artistic creativity, etc.</p> <p>The student's study hours for each learning activity are given as well as the hours of non-directed study according to the principles of the ECTS</p>	Lectures	12
	Laboratories	40
	Bibliography study	33
	Project	12
	Exams	3
	Total	100
<p>STUDENT PERFORMANCE EVALUATION</p> <p>Description of the evaluation procedure</p> <p>Language of evaluation, methods of evaluation, summative or conclusive, multiple choice questionnaires, short-answer questions, open-ended questions, problem solving, written work, essay/report, oral examination, public presentation, laboratory work, clinical examination of patient, art interpretation, other</p> <p>Specifically-defined evaluation criteria are given, and if and where they are accessible to students.</p>	<ul style="list-style-type: none"> • Tests during the labs (30%) • Laboratory reports during the labs(70%) <p>Laboratory report (30%) at the end of the course.</p> <p>A prerequisite for participation in the final laboratory exam is the successful grade in the laboratory performance. The course completes successfully when the final laboratory exam degree is also successful.</p>	

(5) ATTACHED BIBLIOGRAPHY

<p>- Suggested bibliography:</p> <p>- Related academic journals:</p> <ul style="list-style-type: none"> • <i>Digital Systems Design with FPGAs and CPLDs</i>, Ian Grout, Newnes, 2008 • Hands-on Experience with Altera FPGA Development Boards, Parab, Jivan S., Gad, Rajendra S., Naik, G.M, Springer,2018. • Σχεδιασμός ψηφιακών συστημάτων FPGAs, Π. Κίτσος, Ν. Σκλάβος, 2015, Εκδόσεις Πατάκη. [Μετάφραση: Wayne Wolf, FPGA-Based System Design, ISBN: 0-137-03348-6, Prentice Hall Modern Semiconductor Design Series]. • Εισαγωγή στον προγραμματισμό μικροελεγκτών, FPGA και CPLD, Στυλιανός Μπουλταδάκης, Γιώργος Πατουλίδης, Νικόλαος Ασημόπουλος, Εκδόσεις Τζιόλα, 2010 • Εφαρμογές της VHDL για Ηλεκτρονικούς, Π.Κωσταράκης, Γ.Αγγουράς, Πανεπιστημιακό Τυπογραφείο Ιωάννινων, 2003 • Αρχιτεκτονική και προγραμματισμός του AVR, Παναγιώτης Παπάζογλου, Εκδόσεις: Τζιόλα, 2017 • <i>Programming and Customizing the AVR Microcontroller</i>, D.V. Gadre, 2017 • <i>AVR Microcontroller and Embedded Systems: Using Assembly and C</i>, Sepehr Naimi, 2010
