

COURSE OUTLINE

(1) GENERAL

SCHOOL	SCHOOL OF SCIENCES		
ACADEMIC UNIT	DEPARTMENT OF PHYSICS		
LEVEL OF STUDIES	UNDERGRADUATE		
COURSE CODE	502	SEMESTER	6,8
COURSE TITLE	Digital Electronics		
INDEPENDENT TEACHING ACTIVITIES <i>if credits are awarded for separate components of the course, e.g. lectures, laboratory exercises, etc. If the credits are awarded for the whole of the course, give the weekly teaching hours and the total credits</i>	WEEKLY TEACHING HOURS	CREDITS	
	5	4	
<i>Add rows if necessary. The organisation of teaching and the teaching methods used are described in detail at (d).</i>			
COURSE TYPE <i>general background, special background, specialised general knowledge, skills development</i>	General background / special background, Skills development		
PREREQUISITE COURSES:			
LANGUAGE OF INSTRUCTION and EXAMINATIONS:	GREEK		
IS THE COURSE OFFERED TO ERASMUS STUDENTS	YES (in GREEK)		
COURSE WEBSITE (URL)	http://ecourse.uoi.gr/course/view.php?id=9		

(2) LEARNING OUTCOMES

<p>Learning outcomes</p> <p><i>The course learning outcomes, specific knowledge, skills and competences of an appropriate level, which the students will acquire with the successful completion of the course are described.</i></p> <p><i>Consult Appendix A</i></p> <ul style="list-style-type: none"> • <i>Description of the level of learning outcomes for each qualifications cycle, according to the Qualifications Framework of the European Higher Education Area</i> • <i>Descriptors for Levels 6, 7 & 8 of the European Qualifications Framework for Lifelong Learning and Appendix B</i> • <i>Guidelines for writing Learning Outcomes</i> 				
<p>This course introduces students to the important concepts and basic skills of Digital Design and the related analysis of digital circuits</p> <p>Upon successful completion of this course module students possess advanced knowledge, skills and competences in the subject of Digital Electronics that enable them to:</p> <ul style="list-style-type: none"> • Work on different numbering systems • Design and analyze simple combinational and sequential circuits either with discrete gates or more complex IC • Simulate and analyze digital systems by means of modern simulation software • Have a solid background on various types of memory modules and the corresponding circuitry. • Cooperate with fellow students as a team for the successful implementation of the laboratory exercises with the appropriate preparation of the procedures that must be followed, as well as the study of the relevant material for homework 				
<p>General Competences</p> <p><i>Taking into consideration the general competences that the degree-holder must acquire (as these appear in the Diploma Supplement and appear below), at which of the following does the course aim?</i></p> <table style="width: 100%; border: none;"> <tr> <td style="width: 50%; border: none;"><i>Search for, analysis and synthesis of data and information, with the use of the necessary technology</i></td> <td style="width: 50%; border: none;"><i>Project planning and management</i></td> </tr> <tr> <td style="border: none;"></td> <td style="border: none;"><i>Respect for difference and multiculturalism</i></td> </tr> </table>	<i>Search for, analysis and synthesis of data and information, with the use of the necessary technology</i>	<i>Project planning and management</i>		<i>Respect for difference and multiculturalism</i>
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<i>Adapting to new situations</i> <i>Decision-making</i> <i>Working independently</i> <i>Team work</i> <i>Working in an international environment</i> <i>Working in an interdisciplinary environment</i> <i>Production of new research ideas</i>	<i>Respect for the natural environment</i> <i>Showing social, professional and ethical responsibility and sensitivity to gender issues</i> <i>Criticism and self-criticism</i> <i>Production of free, creative and inductive thinking</i> <i>Others...</i>
<ul style="list-style-type: none"> • Search for, analysis and synthesis of data and information, with the use of the necessary technology • Working independently • Team work • Production of free, creative and inductive thinking 	

(3) SYLLABUS

<p>1 NUMBER SYSTEMS AND CODES</p> <p>2. DIGITAL ELECTRONIC SIGNALS AND SWITCHES</p> <p>3. BASIC LOGIC GATES</p> <p>4. PROGRAMMABLE LOGIC DEVICES: CPLDS AND FPGAS WITH VHDL DESIGN</p> <p>5. BOOLEAN ALGEBRA AND REDUCTION TECHNIQUES</p> <p>6. EXCLUSIVE-OR AND EXCLUSIVE-NOR GATES</p> <p>7. ARITHMETIC OPERATIONS AND CIRCUITS</p> <p>8. CODE CONVERTERS, MULTIPLEXERS, AND DEMULTIPLEXERS</p> <p>9. LOGIC FAMILIES AND THEIR CHARACTERISTICS</p> <p>10. FLIP-FLOPS AND REGISTERS</p> <p>11. PRACTICAL CONSIDERATIONS FOR DIGITAL DESIGN</p> <p>12. COUNTER CIRCUITS AND VHDL STATE MACHINES</p> <p>13. SHIFT REGISTERS</p> <p>14. SEMICONDUCTOR, MAGNETIC AND OPTICAL MEMORY (incl. RAM, ROM, PROM, EPROM, EEPROM)</p>
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(4) TEACHING and LEARNING METHODS - EVALUATION

<p style="text-align: center;">DELIVERY</p> <p style="text-align: center;"><i>Face-to-face, Distance learning, etc.</i></p>	<p>Face to face lectures</p> <p>Real time practice</p>												
<p style="text-align: center;">USE OF INFORMATION AND COMMUNICATIONS TECHNOLOGY</p> <p style="text-align: center;"><i>Use of ICT in teaching, laboratory education, communication with students</i></p>	<ul style="list-style-type: none"> • Use of electronic presentation with multimedia contenting class, • Student support through the course webpage and the departmental e-learning platform, • Electronic communication of instructors and students, through the course webpage and bye-mail, • Use of special circuit simulation software. 												
<p style="text-align: center;">TEACHING METHODS</p> <p style="text-align: center;"><i>The manner and methods of teaching are described in detail.</i></p> <p style="text-align: center;"><i>Lectures, seminars, laboratory practice, fieldwork, study and analysis of bibliography, tutorials, placements, clinical practice, art workshop, interactive teaching, educational visits, project, essay writing, artistic creativity, etc.</i></p>	<table border="1" style="width: 100%;"> <thead> <tr> <th style="text-align: center;"><i>Activity</i></th> <th style="text-align: center;"><i>Semester workload</i></th> </tr> </thead> <tbody> <tr> <td>Lectures</td> <td style="text-align: center;">20</td> </tr> <tr> <td>Exercises</td> <td style="text-align: center;">13</td> </tr> <tr> <td>Laboratory experiments</td> <td style="text-align: center;">20</td> </tr> <tr> <td><i>study and analysis of bibliography</i></td> <td style="text-align: center;">44</td> </tr> <tr> <td>exams</td> <td style="text-align: center;">3</td> </tr> </tbody> </table>	<i>Activity</i>	<i>Semester workload</i>	Lectures	20	Exercises	13	Laboratory experiments	20	<i>study and analysis of bibliography</i>	44	exams	3
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<p><i>The student's study hours for each learning activity are given as well as the hours of non-directed study according to the principles of the ECTS</i></p>		
	Course total	100
<p align="center">STUDENT PERFORMANCE EVALUATION</p> <p><i>Description of the evaluation procedure</i></p> <p><i>Language of evaluation, methods of evaluation, summative or conclusive, multiple choice questionnaires, short-answer questions, open-ended questions, problem solving, written work, essay/report, oral examination, public presentation, laboratory work, clinical examination of patient, art interpretation, other</i></p> <p><i>Specifically-defined evaluation criteria are given, and if and where they are accessible to students.</i></p>	<p>Course grade = Final exam (80%) + Homeworks (20%)</p> <p>Final exam is at the end of semester based on Theory Lectures.</p> <p>Homeworks are prepared weekly and report on the analysis of experimental data obtained on each laboratory course</p>	

(5) ATTACHED BIBLIOGRAPHY

<p><i>- Suggested bibliography:</i></p> <ol style="list-style-type: none"> 1. Digital Electronics, William Kleitz (8th Edition) Tziolas publishing, (translated in Greek) ISBN: 978-960-418-3388 2. Digital Electronics, Floyd (8th Edition) Ion publishing, (translated in Greek) ISBN:978-960-411-646-1 3. Electronic Principles, A. Malvino/Leach (7th Edition) Tziolas publishing (translated in Greek) ISBN: 978-960-8129-16-18 4. Laboratory Exercises on Digital Electronics, Kostarakis et al. Laboratory manual (In Greek)
